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(11) **EP 1 137 266 A3**

(12) **EUROPEAN PATENT APPLICATION**

(88) Date of publication A3:
03.12.2003 Bulletin 2003/49

(51) Int Cl.7: **H04N 5/202, G09G 3/20**

(43) Date of publication A2:
26.09.2001 Bulletin 2001/39

(21) Application number: **01302529.1**

(22) Date of filing: **20.03.2001**

(84) Designated Contracting States:
**AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU
MC NL PT SE TR**
Designated Extension States:
AL LT LV MK RO SI

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(30) Priority: **24.03.2000 JP 2000084663**
22.01.2001 JP 2001013864

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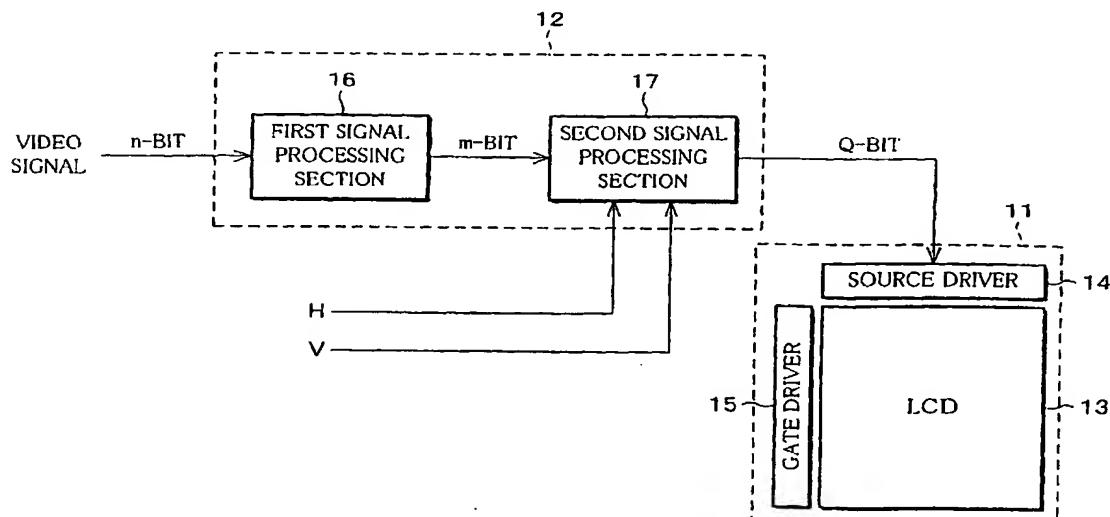
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(54) **Image processing apparatus and image display apparatus using same**

(57) An image processing apparatus of the present invention comprising (a) a first signal processing circuit for applying gamma correction to an n-bit (n : a natural number) digital signal inputted as a video signal and for converting the n-bit digital signal into an m-bit ($m > n$, m : a natural number) digital signal, and (b) a second sig-

nal processing circuit for adding a noise signal, which is used for pseudo contour reduction, into the m-bit digital signal from the first signal processing circuit and for outputting a Q-bit (Q : a natural number) digital signal, which is obtained from rounding off a less significant ($m - Q$) bit ($Q \leq n$) from the m-bit digital signal, to a display section.

FIG. 1



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EUROPEAN SEARCH REPORT

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EP 01 30 2529

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The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 7 October 2003	Examiner Corsi, F
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EPO FORM 1500 (3. 82 (P04C01))



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